Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(delay adj1 lock$3 adj1 loop same fine adj1 delay same coarse adj1 delay same (logic or combin$4 or combination or both) same shift$4 same select$4).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 14:35
L2	1	(delay adj1 lock$3 adj1 loop same fine adj1 delay same coarse adj1 delay same (logic) same shift$4 same select$4).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 14:35
L3	1	(delay adj1 lock$3 adj1 loop same fine adj1 delay same coarse adj1 delay same (logic$4) same shift$4 same select$4).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 14:35
L4	0	(delay adj1 lock$3 adj1 loop same fine adj1 delay same coarse adj1 delay same (both) same shift$4 same select$4).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 14:36
L5	0	(delay adj1 lock$3 adj1 loop same fine adj1 delay same coarse adj1 delay same (comb$4) same shift$4 same select$4).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 14:36
L6	0	(delay adj1 lock$3 adj1 loop same fine adj1 delay same coarse adj1 delay same (combin$4) same shift$4 same select$4).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 14:36
L7	0	(delay adj1 lock$3 adj1 loop same fine adj1 delay same coarse adj1 delay same (combin$6) same shift$4 same select$4).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 14:36
L8	0	(delay adj1 lock$3 adj1 loop same fine adj1 delay same coarse adj1 delay same (combination) same shift$4 same select$4).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 14:36

Interference Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3485	(plurality or multiple) with (memory or cell$3 or memory with cells) and (DLL or delay with lock$4 with loop$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 09:37
S2	3242	(plurality or multiple) with (memory or cell$3 or memory with cells) and (DLL or delay adj4 lock$4 adj3 loop$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 09:37
S3	3203	(plurality or multiple) with (memory or cell$3 or memory with cells) and (DLL or delay adj lock$4 adj loop$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 09:37
S4	273	(plurality or multiple) with (memory or cell$3 or memory with cells) with (DLL or delay adj lock$4 adj loop$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 09:37
S5	90	(plurality or multiple) adj4 (memory or cell$3 or memory with cells) with (DLL or delay adj lock$4 adj loop$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 09:38
S6	20	(plurality or multiple) adj4 (memory with cells) with (DLL or delay adj lock$4 adj loop$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/30 09:38

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	unequal with delay with line with coarse with fine	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 16:05
S2	37	(unequal or variable) with delay with line with coarse with fine	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 16:07
S3	9	(unequal or variable) with delay with line with coarse with fine with select$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:13
S4	13	(("4922141") or ("5295164") or ("5336940") or ("5684421") or ("6101197") or ("6327318") or ("6445231") or ("6472921") or ("6480047") or ("6483359") or ("6489823") or ("6531925") or ("6628154")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:14
S5	16	(("4330750") or ("5146121") or ("5382921") or ("5416436") or ("5744991") or ("5994934") or ("6046620") or ("6100733") or ("6101197") or ("6144713") or ("6157691") or ("6222894") or ("6242955") or ("6297680") or ("6327318") or ("6333959")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:21
S6	61	delay adj3 lock$3 adj loop and shift with register and logic with circuit and fine with coarse	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:22
S7	58	delay adj3 lock$3 adj loop and shift with register and logic with circuit and fine with coarse and (select$4 or mux$6)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:32
S8	5	delay adj3 lock$3 adj loop and shift with register and logic with circuit with coarse and fine same (select$4 or mux$6)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:33
S9	6	delay adj3 lock$3 adj loop and shift with register and logic with circuit with coarse and fine and (select$4 or mux$6)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:35
S10	6	delay adj3 lock$3 adj loop and shift with register and logic with circuit with coarse and fine	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:35

S11	11	delay adj3 lock$3 adj loop and shift with register and logic with circuit same coarse and fine	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:37
S12	16	delay adj3 lock$3 adj loop and shift with register and logic with (circuit or unit) same coarse and fine	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/25 18:38